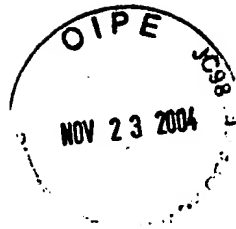


Docket No.: 57454-132



PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES**

In re Application of	:	Customer Number: 20277
Akira YAMADA	:	Confirmation Number: 5917
Application No.: 09/874,239	:	Tech Center Art Unit: 2188
Filed: June 06, 2001	:	Examiner: Thang H. Ho

For: SYNCHRONOUS SIGNAL PRODUCING CIRCUIT FOR CONTROLLING A DATA  
READY SIGNAL INDICATIVE OF END ACCESS TO A SHARED MEMORY AND  
THEREBY CONTROLLING SYNCHRONIZATION BETWEEN PROCESSOR AND  
COPROCESSOR

**NOTICE OF APPEAL  
FROM THE PRIMARY EXAMINER  
TO THE BOARD OF APPEALS**

Mail Stop Notice of Appeal  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant hereby appeals to the Board of Appeals from the decision dated June 23, 2004 of  
the Primary Examiner finally rejecting claims 1-4, 6, 8-11, 13 and 15-20.

Appeal Fee: \$ \$340.00

- ☐ Not required (fee paid in prior appeal in this application).  
☒ Charge to Deposit Account No. 500417.

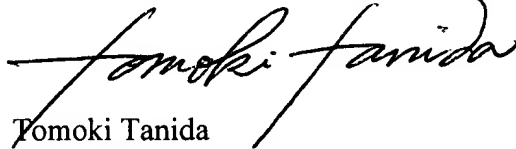
11/24/2004 EABUBAK1 00000047 500417 09874239

02 FC:1401 340.00 DA

☐ Check Enclosed.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

A handwritten signature in black ink, appearing to read "Tomoki Tanida", written over the printed name.

Tomoki Tanida  
Recognition Under 37 CFR 10.9(b)

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
Phone: 202.756.8000 SAB:TT:lnm  
Facsimile: 202.756.8087  
**Date: November 23, 2004**

**Please recognize our Customer No. 20277  
as our correspondence address.**

WDC99 1009472-1.057454.0132